

AMENDMENTS TO THE SPECIFICATION

Please replace the last full paragraph beginning on page 6, line 21 of the application as filed, with the following amended paragraph:

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a In the embodiment shown in Fig. 2, the power planes are partitioned such that a majority of the logic is powered off during the S3 state, as described. However, system memory 204 and logic required to wake the system, such as portion 217 of south bridge 203, portion 219 of graphics card ~~248~~ 226 and portion 220 of the PCI devices 221, remain powered during the S3 state. The exemplary computer system in Fig. 2 may use a power supply that works with the partitioned power planes such an ATX type power supply and logic/FETs on the motherboard to create voltage rails with the desired characteristics described in the tables 1 and 2 below.

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